Header File

//-------------------------------------------------------------------

// Author : Onur Tigli

// Date : September 10, 2013

// File Name : lfsr\_gen\_my.v

// Name of Design Units in file : lfsr\_gen\_my

// Purpose of Code : This module implements the linear

// feedback shift register to generate

// random binary data for the waveform

// generator module to modulate (MSK) and

// transmit this modulated data

// Description of hardware model: 8-bit tap of FFs generates a // pseudo random sequence.

// Limitations of Model : N/A

// Known Errors : None

// Intended Design Library of compiled code : een414/my\_design

// List of Analysis Dependencies: N/A

// Environment : Xilinx ISE v?.?

// Simulator : IrSim, Mentor Graphics (ModelSim)

// Version : ?.?

// Platform : NT, Linux, Windows etc.

// Change List : N/A

//--------------------------------------------------------------------

module lfsr\_gen\_my(clk,reset\_n,data\_out);

input clk;